

WHAT IS CLAIMED IS:

1. An apparatus for processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

a data pipeline having an input for receiving the encapsulation packet formatted as a sequence of parallel data segments, said data pipeline including a plurality of pipeline stages, each said pipeline stage for holding therein successive ones of said data segments;

a modifier coupled to said data pipeline for modifying said encapsulating header in response to first information contained in said encapsulated packet; and

a router coupled between said data pipeline and said modifier, said router having an input for receiving selectively programmable second information indicative of a location of said first information within said encapsulated packet, said router responsive to said second information for routing said first information from said data pipeline to said modifier.

2. The apparatus of Claim 1, wherein said modifier is for removing header information from said encapsulating header.

3. The apparatus of Claim 2, wherein said modifier is for replacing said removed header information with said first-mentioned information.

4. The apparatus of Claim 1, wherein said router includes a selector having an input coupled to said pipeline and having an output, and a shifter having an input coupled to said selector output and having an output coupled to said modifier.

5. The apparatus of Claim 4, wherein said modifier includes a selector having an input coupled to said pipeline and to said output of said shifter.

6. The apparatus of Claim 1, wherein said encapsulation packet is an OSI layer 2 packet, said encapsulating header is an OSI layer 2 header and said encapsulated packet is an OSI layer 3 packet.

7. The apparatus of Claim 1, wherein said first information includes address information.

8. An apparatus for processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

a data pipeline having an input for receiving the encapsulation packet formatted as a sequence of parallel data segments having a common data segment width, said data pipeline including a plurality of pipeline stages, each said pipeline stage for holding therein successive ones of said data segments, one of said pipeline stages having a data width that is greater than said common data segment width for holding therein a portion of the encapsulation packet that is larger than said data segments;

a modifier coupled to said data pipeline for modifying said encapsulating header in response to information contained in said encapsulated packet; and

a router coupled between said data pipeline and said modifier for routing said information from said data pipeline to said modifier.

9. The apparatus of Claim 8, wherein said portion of the encapsulation packet includes one of said data segments and part of another of said data segments.

10. The apparatus of Claim 9, wherein said part of said another data segment includes a portion of said information.

11. The apparatus of Claim 8, wherein said one and another data segments are adjacent one another in said sequence.

12. The apparatus of Claim 11, wherein said another data segment follows said one data segment in said sequence.

13. An apparatus for processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

a data pipeline having an input for receiving the encapsulation packet formatted as a sequence of parallel data segments, said data pipeline including a plurality of pipeline stages, each said pipeline stage for holding therein successive ones of said data segments,

one of said pipeline stages coupled to another of said pipeline stages for combining, in said another pipeline stage, part of a data segment currently held in said one pipeline stage with a data segment currently held in said another pipeline stage;

a modifier coupled to said data pipeline for modifying said encapsulating header in response to information contained in said encapsulated packet; and

a router coupled between said data pipeline and said modifier for routing said information from said data pipeline to said modifier.

14. The apparatus of Claim 13, wherein said one pipeline stage and said another pipeline stage are adjacent one another in said data pipeline.

15. The apparatus of Claim 14, wherein said one pipeline stage is upstream from said another pipeline stage in said data pipeline.

16. The apparatus of Claim 13, wherein said part of said data segment currently held in said one pipeline stage includes a portion of said information.

17. A method of processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

receiving the encapsulation packet formatted as a sequence of parallel data segments, said encapsulated packet including information for use in modifying said encapsulating header;

insuring that said information is available in parallel format, including combining in parallel a first of said parallel data segments and part of a second of said parallel data segments at a temporal position in said sequence occupied by said first parallel data segment; and

modifying said encapsulating header based on said parallel-formatted information.

18. The method of Claim 17, wherein said first and second parallel data segments are adjacent one another in said sequence.

19. The method of Claim 18, wherein said second parallel data segment follows said first parallel data segment in said sequence.

20. The method of Claim 17, wherein said part of said second parallel data segment includes a portion of said information.

21. A method of processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

receiving the encapsulation packet formatted as a sequence of parallel data segments;

receiving selectively programmable first information indicative of a location of second information within said encapsulated packet;

based on said first information, obtaining said second information from said encapsulated packet; and

modifying said encapsulating header based on said second information.

22. An apparatus for processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

means for receiving the encapsulation packet formatted as a sequence of parallel data segments, said encapsulated packet including information for use in modifying said encapsulating header;

means for insuring that said information is available in parallel format, including means for combining in parallel a first of said parallel data segments with a portion of a second of said parallel data segments; and

means for modifying said encapsulating header based on said parallel-formatted information.

23. An apparatus for processing an encapsulation packet including an encapsulating header and an encapsulated packet, comprising:

means for receiving the encapsulation packet formatted as a sequence of parallel data segments;

means for receiving selectively programmable first information indicative of a location of second information within said encapsulated packet;

means for obtaining said second information from said encapsulated packet based on said first information; and

means for modifying said encapsulating header based on said second information.